



Solid State Devices, Inc.

14701 Firestone Blvd * La Mirada, CA 90638
 Phone: (562) 404-4474 * Fax: (562) 404-1773
 ssdi@ssdi-power.com * www.ssdi-power.com

Designer's Data Sheet

Part Number / Ordering Information^{1/}

SSR2010 CT **S1**

—

L

Screening^{2/}

— = Not Screened

TX = TX Level

TXV = TXV Level

S = S Level

L

Package

S1 = SMD1

SSR2010CTS1

20 AMP
100 VOLTS
POSITIVE CENTERTAP
SCHOTTKY RECTIFIER

FEATURES:

- PIV: 100 Volts
- Low Forward Voltage Drop
- Low Reverse Leakage
- Hermetically Sealed Power Surface Mount Package
- Guard Ring for Overvoltage Protection
- Eutectic Die Attach
- 175°C Operating Junction Temperature
- TX, TXV, or Space Level Screening Available

MAXIMUM RATINGS

RATING	SYMBOL	VALUE	UNIT
Peak Repetitive Reverse Voltage and DC Blocking Voltage	SSR2010CT V_{RRM} V_{RWM} V_R	100	Volts
Average Rectified Output Current ^{3/} (Resistive Load, 60Hz, Sine Wave, TA=25°C)	I_O	20	Amps
Peak Surge Current ^{3/} (8.3 ms Pulse, Half Sine Wave superimposed on I_O , allow junction to reach equilibrium between pulses, TA=25°C)	I_{FSM}	300	Amps
Operating and Storage Temperature	T_{OP} & T_{STG}	-65 to +175	°C
Maximum Thermal Resistance ^{3/} Junction to Case	$R_{\theta JC}$	0.8	°C/W

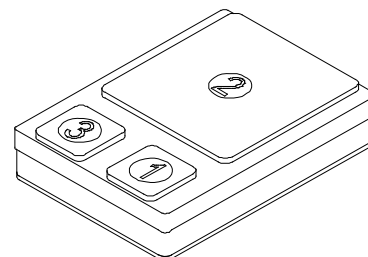
Note:

^{1/} For ordering information, price, and availability, contact factory.

^{2/} Screening based on MIL-PRF-19500. Screening flows available on request.

^{3/} Both legs tied together.

SMD1



NOTE: All specifications are subject to change without notification.
 SCD's for these devices should be reviewed by SSDI prior to release.

DATA SHEET #: RS0067E

DOC



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SSR2010CTS1

ELECTRICAL CHARACTERISTICS (Per Leg)

CHARACTERISTICS	SYMBOL	MAXIMUM	UNIT
Instantaneous Forward Voltage Drop ($I_F = 3 \text{ Adc}$, $T_A = 25^\circ\text{C}$, Pulse) ($I_F = 5 \text{ Adc}$, $T_A = 25^\circ\text{C}$, Pulse) ($I_F = 10 \text{ Adc}$, $T_A = 25^\circ\text{C}$, Pulse)	V_{F1} V_{F2} V_{F3}	0.68 0.7 0.77	Vdc
Instantaneous Forward Voltage Drop ($I_F = 5 \text{ Adc}$, $T_A = -55^\circ\text{C}$, Pulse)	V_{F4}	0.84	Vdc
Reverse Leakage Current (Rated V_R , $T_A = 25^\circ\text{C}$, Pulse)	I_{R1}	100	μA
Reverse Leakage Current (Rated V_R , $T_A = 100^\circ\text{C}$, Pulse)	I_{R2}	5	mA
Junction Capacitance ($V_R = 10 \text{ Vdc}$, $T_A = 25^\circ\text{C}$, $f = 1 \text{ MHz}$)	C_J	400	pF

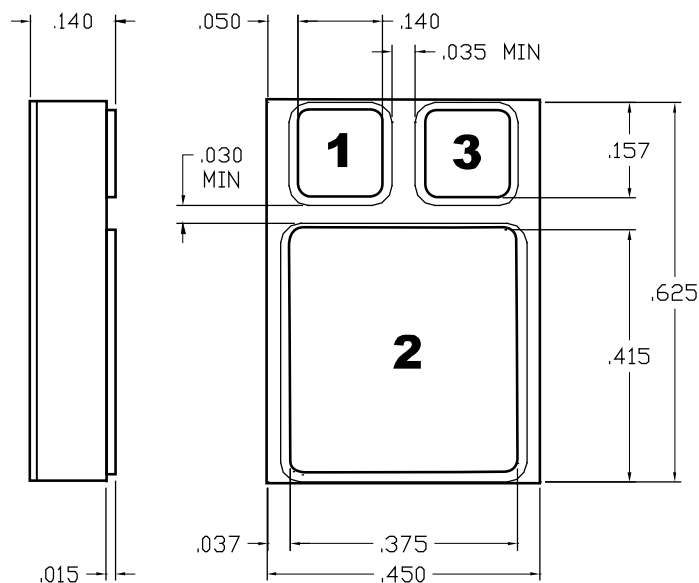
Case Outline- SMD1

PIN OUT:

PIN 1- ANODE 1

PIN 2- CATHODE

PIN 3- ANODE 2



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