

**Solid State Devices, Inc.**

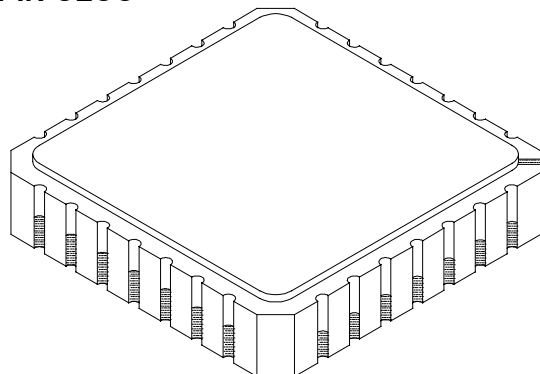
14701 Firestone Blvd * La Mirada, Ca 90638

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SSR2010CT-28**Designer's Data Sheet****FEATURES:**

- PIV: 100 Volts
- Low Forward Voltage Drop
- Low Reverse Leakage
- Hermetically Sealed Surface Mount Package
- Guard Ring for Overvoltage Protection
- Eutectic Die Attach
- 175°C Operating Junction Temperature
- TX, TXV, and Space Level Screening Available

**20 AMPS
100 VOLTS
CENTERTAP
SCHOTTKY
RECTIFIER****28 PIN CLCC**

MAXIMUM RATINGS		Symbol	Value	Units
Peak Repetitive Reverse Voltage and DC Blocking Voltage	SSR2010CT-28	V_{RRM} V_{RWM} V_R	100	Volts
Average Rectified Forward Current (Resistive Load, 60 Hz, Sine Wave, $T_A=25^{\circ}\text{C}$) ^{1/}		I_O	20	Amps
Peak Surge Current (8.3 ms Pulse, Half Sine Wave Superimposed on I_O , allow junction to reach equilibrium between pulses, $T_A=25^{\circ}\text{C}$) ^{1/}		I_{FSM}	300	Amps
Operating and Storage Temperature		T_{OP} & T_{stg}	-65 to +175	°C
Maximum Thermal Resistance Junction to Case ^{1/}		$R_{\theta JC}$	3.0	°C/W

Notes: ^{1/} Both Legs Tied Together.**NOTE:** All specifications are subject to change without notification.
SCD's for these devices should be reviewed by SSDI prior to release.**DATA SHEET #: RS0085C****DOC**



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ELECTRICAL CHARACTERISTICS (Per Leg)		Symbol	Max	Unit
Instantaneous Forward Voltage Drop ($T_A = 25^\circ\text{C}$, Pulse)	$I_F = 3\text{ Amps}$	V_{F1}	0.7	Volts
	$I_F = 5\text{ Amps}$	V_{F2}	0.72	
	$I_F = 10\text{ Amps}$	V_{F3}	0.82	
Instantaneous Forward Voltage Drop ($I_F = 5\text{ Amps}$, $T_A = -55^\circ\text{C}$, Pulse)		V_{F4}	0.87	Volts
Reverse Leakage Current (Rated V_R , $T_A = 25^\circ\text{C}$, Pulse)		I_{R1}	100	μA
Reverse Leakage Current (Rated V_R , $T_A = 100^\circ\text{C}$, Pulse)		I_{R2}	5.0	mA
Junction Capacitance ($V_R = 10\text{ V}_{\text{DC}}$, $T_A = 25^\circ\text{C}$, $f = 1\text{ MHz}$)		C_J	400	pF

CASE OUTLINE: 28 PIN CLCC

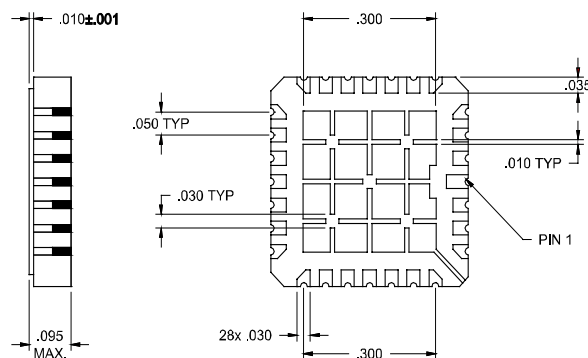
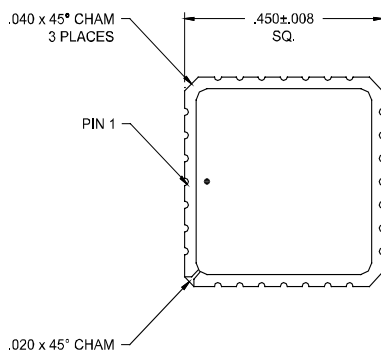
PIN OUT: ¹

PIN 5-11: CATHODE

PIN 1, 15-28: ANODE 1

PIN 2, 3, 13, 14: ANODE 2

Note 1- For optimal performance, connect together same terminal pins: Cathode, Anode 1, & Anode 2



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