

Solid State Devices, Inc.

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Designer's Data Sheet Part Number/Ordering Information 1/ SDR90 ^T Screening ^{2/} = Not Screened $\overline{TX} = TX \text{ Level}$ TXV = TXV Level S = S Level **Package** = DO-5 Family/Voltage 0 = 50 V 3 = 300 V4 = 400 V1 = 100 V 2 = 200 V5 = 500 V

SDR900 thru SDR905

30 Amp
ULTRAFAST RECTIFIER
50 – 500 Volt
50 nsec

Features:

- Ultrafast recovery: 50 nsec maximum
- Low reverse leakage
- Hermetically sealed
- High surge current
- Single chip construction
- Available in isolated package
- Facility (Calaba package
- For high efficiency applications
- TX, TXV, and S-level screening available Contact factory ^{2/}

Maximum Ratings		Symbol	Value	Units
Peak Repetitive Reverse and DC Blocking Voltage	SDR900 SDR901 SDR902 SDR903 SDR904 SDR905	V _{RRM} V _{RWM} V _R	50 100 200 300 400 500	v
Average Rectified Forward Current (Resistive Load, 60 Hz, Sine Wave, T _A = 25°C)		lo	30	Α
Peak Surge Current (8.3 ms Pulse, Half Sine Wave, T _A = 25°C)		I _{FSM}	350	Α
Operating & Storage Temperature		T _{OP} & T _{STG}	-65 to +175	°C
Maximum Thermal Resistance Junction to Case		R _{eJC}	1.0	°C/W

Notes:

- 1/ For ordering information, price, operating curves, and availability contact factory.
- 2/ Screening based on MIL-PRF-19500. Screening flows available on request.

DO-5





SDR900 thru SDR905

Electrical Characteristics		Symbol	Max	Units
Instantaneous Forward Voltage Drop (T _A = 25°C, 300 µs pulse)	$I_F = 30 A_{DC}$	V _{F1}	1.45	V _{DC}
Instantaneous Forward Voltage Drop (T _A = -55°C, 300 µs pulse)	I _F = 30 A _{DC}	V _{F2}	1.6	V _{DC}
Reverse Leakage Current (Rated V _R , T _A = 25°C, 300 μs pulse minimum)		I _{R1}	50	μΑ
Reverse Leakage Current (Rated V _R , T _A = 100°C, 300 μs pulse minimum)	I _{R2}	10	mA
Junction Capacitance (T _A = 25°C, f = 1 MHz)	$V_R = 10 V_{DC}$	Сл	250	pf
Reverse Recovery Time $(I_F = 500 \text{ mA}, I_R = 1 \text{ A}, I_{RR} = 250 \text{ mA}, T_A = 25^{\circ}\text{C}$	C)	t _{RR}	50	nsec

