



Solid State Devices, Inc.

14701 Firestone Blvd. * La Mirada, Ca 90638
Phone: (562) 404-4474 * Fax: (562) 404-1773
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SED40HE45LVCT Series

Designer's Data Sheet

Part Number / Ordering Information^{1/}

SED40HE45LVCT

L Screening^{2/}

__ = None
TX = TX Level
TXV = TXV Level
S = S Level

L Lead Orientation

1 = Front Mount
2 = Side Mount
3 = Split Mount

**SURFACE MOUNT
40 AMP, 45 VOLTS
LOW VF CENTERTAP
SCHOTTKY RECTIFIER**

FEATURES:

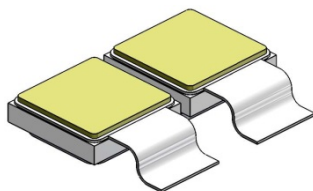
- Low Reverse Leakage
- Low Forward Voltage Drop
- Hermetically Sealed Power Surface Mount Package
- Guard Ring for Overvoltage Protection
- Eutectic Die Attach
- 175°C Operating Temperature
- Hot Case Version (Isolated Versions Available – Contact Factory)
- Smaller Footprint than TO-25X package
- Weight: 0.65 g typ
- TX, TXV, and Space Level Screening Available^{2/}

MAXIMUM RATINGS (per leg)	Symbol	Value	Units
Peak Repetitive Reverse Voltage and DC Blocking Voltage	V_{RRM} V_{RWM} V_R	45	Volts
Average Rectified Forward Current (Resistive Load, 60 Hz, Sine Wave, $T_A = 100^\circ\text{C}$)	I_O	20	Amps
Peak Surge Current (8.3 ms Pulse, Half Sine Wave, 1 pulse, $T_A = 25^\circ\text{C}$)	I_{FSM}	250	Amps
Operating and Storage Temperature	T_{OP} & T_{stg}	-55 to +175	°C
Maximum Thermal Resistance Junction to Case	$R_{\theta JC}$	1.25	°C/W

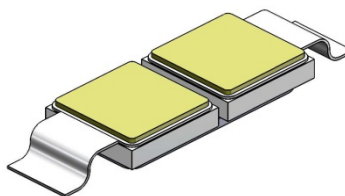
Notes:

1/ For ordering information, price, operating curves, and availability – contact factory.
2/ Screening based on MIL-PRF-19500. Screening flows available on request.

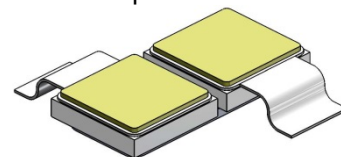
SED40HE45LVCT1
Front Mount



SED40HE45LVCT2
Side Mount



SED40HE45LVCT3
Split Mount



NOTE: All specifications are subject to change without notification.
SCD's for these devices should be reviewed by SSDI prior to release.

DATA SHEET #: SH0104A

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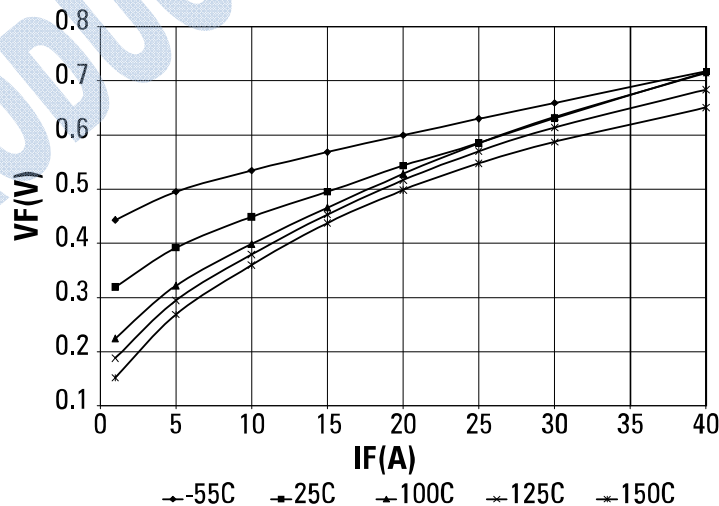
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ELECTRICAL CHARACTERISTICS (per leg)		Symbol	Typical	Maximum	Unit
Instantaneous Forward Voltage Drop ($I_F = 5 A_{DC}$, 300 - 500 μ sec pulse)	$T_A = -55^\circ C$	V_{F1}	0.45	-	V_{DC}
	$T_A = 25^\circ C$	V_{F2}	0.40	0.425	
	$T_A = 125^\circ C$	V_{F3}	0.30	-	
Instantaneous Forward Voltage Drop ($I_F = 10 A_{DC}$, 300 - 500 μ sec pulse)	$T_A = -55^\circ C$	V_{F4}	0.54	-	V_{DC}
	$T_A = 25^\circ C$	V_{F5}	0.45	0.48	
	$T_A = 125^\circ C$	V_{F6}	0.38	0.43	
Instantaneous Forward Voltage Drop ($I_F = 20 A_{DC}$, 300 - 500 μ sec pulse)	$T_A = -55^\circ C$	V_{F7}	0.60	-	V_{DC}
	$T_A = 25^\circ C$	V_{F8}	0.55	0.59	
	$T_A = 125^\circ C$	V_{F9}	0.52	-	
Reverse Leakage Current (Rated V_R , 300 μ sec pulse minimum)	$T_A = 25^\circ C$	I_{R1}	0.125	1	mA
	$T_A = 100^\circ C$	I_{R2}	20	-	
	$T_A = 125^\circ C$	I_{R3}	25	50	
Junction Capacitance ($T_A = 25^\circ C$, $f = 1$ MHz)	$V_R = 5V$	C_{J1}	720	900	pF
	$V_R = 10V$	C_{J2}	540	-	

SED40HE45LV (per leg): V_F (typical) vs. I_F



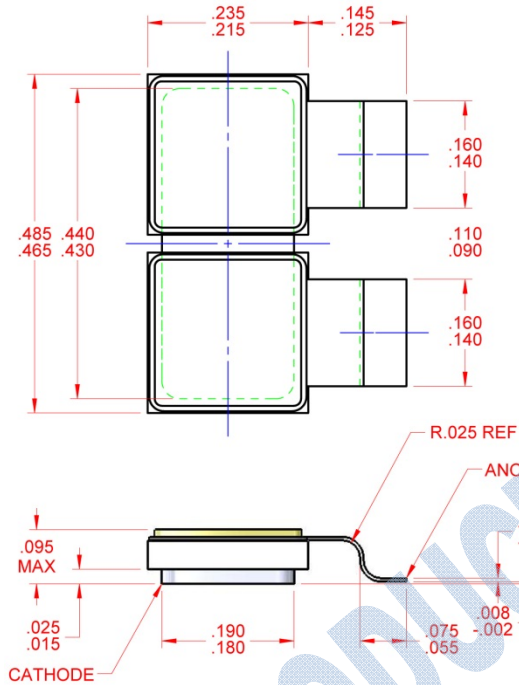


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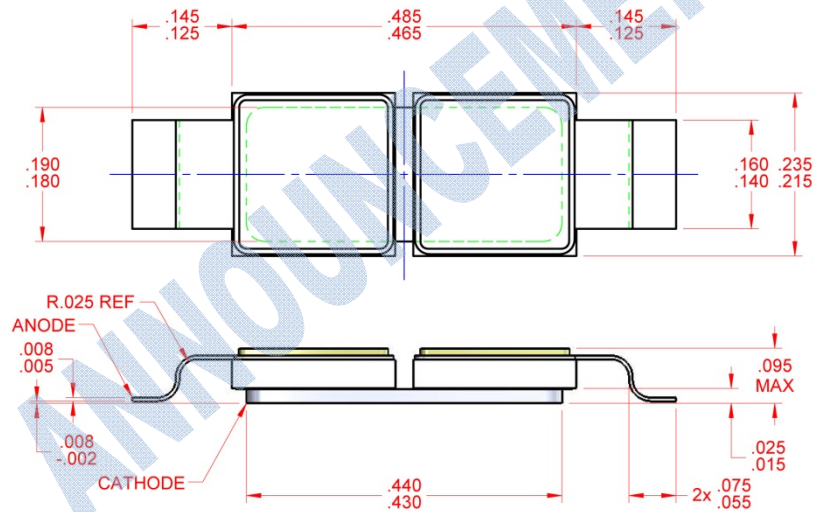
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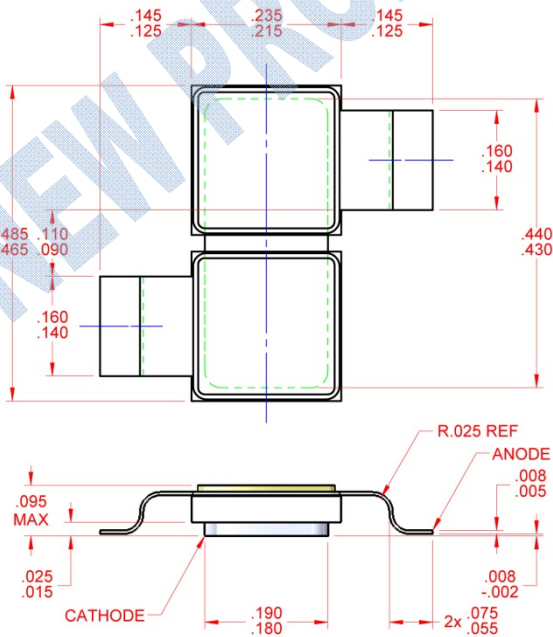
CASE OUTLINE: SED40HE45LVCT1



CASE OUTLINE: SED40HE45LVCT2



CASE OUTLINE: SED40HE45LVCT3



Configuration	Base	Leads
Common Cathode (CT)	Cathode	Anode

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